

AMENDMENTS TO THE CLAIMS

Please amend the claims as follows:

Listing of Claims:

1. (Currently Amended) A capacitance measurement circuit comprising:

first, second and third terminals, said first terminal being accompanied by a first capacitance including first and second capacitance components to be measured and a non-target capacitance component not to be measured, said third terminal being accompanied by a dummy capacitance operating as a dummy of having the same capacitance value as said non-target capacitance component;

a first current detector detecting a first current supplied to said first terminal;

a second current detector detecting a second current induced from said second terminal;

a third current detector detecting a third current supplied to said third terminal; and

a target capacitance forming section formed between said first terminal and said second terminal so that said first terminal is accompanied by said first capacitance component, said target capacitance forming section, said first to third terminals, and said first to third current detectors comprising constituting a capacitance measurement section; and

means coupled to the capacitance measurement section for providing a capacitance value of the second capacitance component by calculating the outputs of said first to third current detectors.

2. (Original) The capacitance measurement circuit according to claim 1, wherein said first to third current detectors include at least one transistor, said at least one transistor including a transistor which has a transistor characteristic of being less apt to cause a leakage current than an ordinary transistor which constitutes a logic circuit.